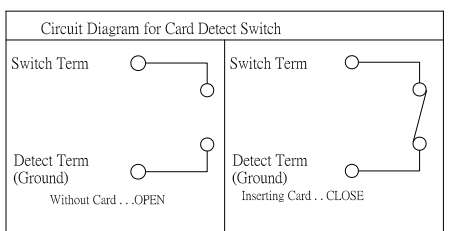
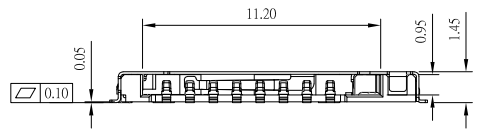


PCB LAYOUT

- PROHIBITED AREA
- SOLDERPROHIBITED AREA
- PATTERN PROHIBITED AREA



Pin No	Name	Type	DESCRIPTION
TERM1	DAT2	I/O/PP	Date Line[bit 2]
TERM2	CD/DAT3	I/O/PP	Card Detect Date Line[bit 3]
TERM3	CMD	PP	Command Response
TERM4	VDD	S	Supply Voltage
TERM5	CLK	I	Clock
TERM6	VSS	S	Supply Voltage Ground
TERM7	DAT0	I/O/PP	Date Line[bit 0]
TERM8	DAT1	I/O/PP	Date Line[bit 1]

SPECIFICATION

- Electrical :
- Current Rating: 0.5A
 - Voltage Rating: 100V DC
 - Contact Resistance: 100 mΩ Max.
 - Insulation Resistance: 1000 MΩ Min.
 - Dielectric Withstanding Voltage: 500V AC

Material:

- Housing: LCP
 - Contact: Phosphor Bronze
 - Shell: SUS
- Finish:
- Contact: Plated Gold in Mating Area ;
Tin Plated on Solder Balls ;
Nickel under plated overall
 - Shell: Nickel under Plated surface layer



TOLERANCE UNLESS OTHERWISE STATED :

Up to 5	±0.2
Above 5 ~ 15	±0.3
Above 15 ~ 30	±0.4
Above 30 ~ 50	±0.5
Angle	±0.3°

3RD. ANGEL'S UNITS MM

DRAWN BY:	DATE	MAT'L	TITLE	CONNECTOR
Jack Lu	06/20/24	FINISH	MODLE	MICRO SD CARD PUSH H1.45
CHECKED BY:	DATE	SCALE	DWG NO.	SD-TF22AP401-S267
Jacky Chen	06/20/24	1 : 1	PART NO.	SD-TF22AP401-S267
APPROVED BY:	DATE	SHEET NO.	1 of 1	
Tony Kao	06/20/24			

ITEM NO.	DESCRIPTION	DRAWN	DATE